

BIO DATA

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DESIGNATION: Assistant Professor
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EDUCATION DETAILS:

Qualification	Specialization	Institute	Year of Award
M.Tech	VLSI Design & Embedded system	BVB, Hubballi	2016
B.E.	Electrical & Electronics Engg.	BEC, Bagalkot	2008

Title of M.Tech Thesis: Interference cancellation in MIMO system Using Equalization Techniques.

Title of B.E. Project: Design & Implementation of ALU using VLSI.

AREAS OF INTEREST/RESEARCH: VLSI, Real Time Embedded System, MEC.

ACADEMICS:

Sl.No	Subjects Taught
1	ARM Micro-controller
2	VLSI Design
3	Embedded System
4	Operating System
5	Object oriented programming with C++
6	Network Analysis
7	Basic Electrical

EXPERIENCE (In Years Teaching/Industry):7.5 yrs

PUBLICATIONS:

National Journals:01 (Fabrication of MEC devices)

ADMINISTRATIVE RESPONSIBILITIES:

Sl. No.	Nature of Work (Chairman, Coordinator, member etc.)	Worked for (University, College, etc.)	Period
1	Warden	B.v.v. Sangha's, Sri Sharadhadevi Ladies Hostel, MUDHOL	01/08/2016 - Till date
2	AICTE, VTU Coordinator	BGMIT Mudhol	2016-17
3	Alumini Coordinator	BGMIT Mudhol	2016- Till date

WORKSHOPS ATTENDED:

Sl. No	Name of Workshop	Venue	Date (No. of Days)
1	Real Time Embedded System	BVB Hubballi	05
2	Human Resource Development	BVB Hubballi	03
3			